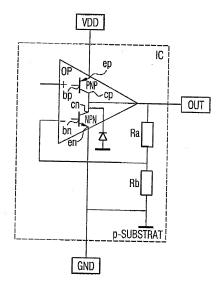
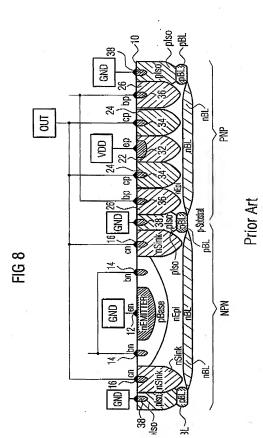
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FIG 7

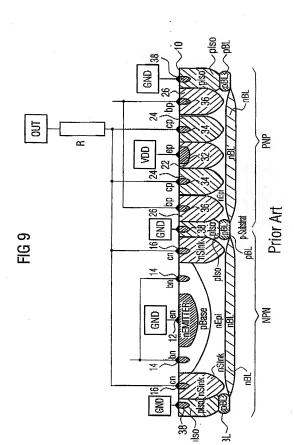


Prior Art

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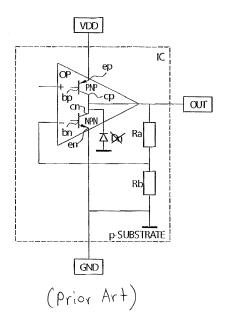


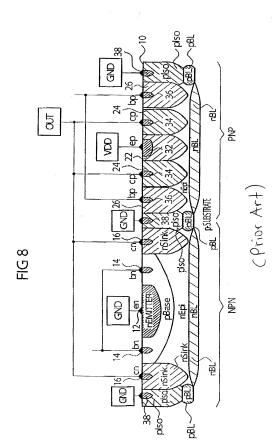
Semiconductor Circuit with Protective Circuit Inventor: Udo Ausserlechner Attorney Docket: 068758.0195 Replacement Page 9 of 9



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FIG 7





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